RAJA NATARAJAN, *Reasoning about synchronization protocols.*
School of Technology & Computer Science, Tata Institute of Fundamental Research,
Homi Bhabha Road, Mumbai 400 005, India.
E-mail: raja@tifr.res.in.
URL Address: www.tcs.tifr.res.in/~raja.

Synchronization protocols, especially those that handle multiway synchronization,
have been an active area of research for several years. Although a number of mechanisms to achieve multiway synchronization have been proposed [1, 2] – some of the synchronization aspects of protocols have been shown to contain subtle errors [3, 4]. Thus it is desirable to have a logic to reason about the correctness of such protocols. We develop a logic for the formal analysis of binary and multiway synchronization protocols, with and without the preemption feature.


